EE 491 WEEKLY REPORT 9

Date: 11/07/16

May1733

**Remote Wafer Testing** 

**IBM**, Geiger

Antonio Montoya, Team Leader

Braden Rosengren: Hardware Lead

**Chris Little: Key Concept Holder** 

**Christian Hurst: Web Master** 

Ben Wiggins: Communications Lead

## • Weekly Summary (Short summary about what you did this week)

This week, the software team continued to develop the webserver by designing the interface between the webserver and the test runner. The hardware team began designing revision 1 of the breakout board and designed a basic bilateral variable level shifter. In addition, they have also made a short list of possible DUT that we can use in our proof of concept. Both teams spent a lot of time working on our Design Documentation for our project.

## • Past week accomplishments (please describe as what was done, by whom, when)

- First revision of the Design Document was completed
- First revision of the variable level shifter was designed
- DUT have been picked for the proof of concept
- Pending issues (if applicable)
  - None

## **Individual contributions**

NAME	Individual Contributions	Hours this	<u>HOURS</u> <u>cumulative</u>
		<u>week</u>	
Antonio	Edited Design Document, surveyed	11	41.5
	necessary protocols for integrating with		
	lab equipment. Met with Geiger		
Christian	Worked on design documentation,	14	46.5
	designed interface between test runner		
	and web server. Met with Geiger		
Ben	Worked on design document. Met with	9	40
	Geiger		
Chris	Compiled DUT part list	3	23.5
Braden	Designed revision one of PCB and level	11	29
	shifter		

## • Plan for coming week (please describe as what, who, when)

Braden: Put the finishing touches on revision one of the breakout board

Ben: Continue learning Python. Work on the test runner. Try and get a response from the Grad student who has experience with the wafer prober

Chris: Order samples of DUT, assist with board production

Christian: Continue to develop the interface between the test runner and the webserver. Work on the test runner

Antonio: Assist with the creation of the test runner. Continue to edit design documents and project plan